PRINT OF DRAWINGS
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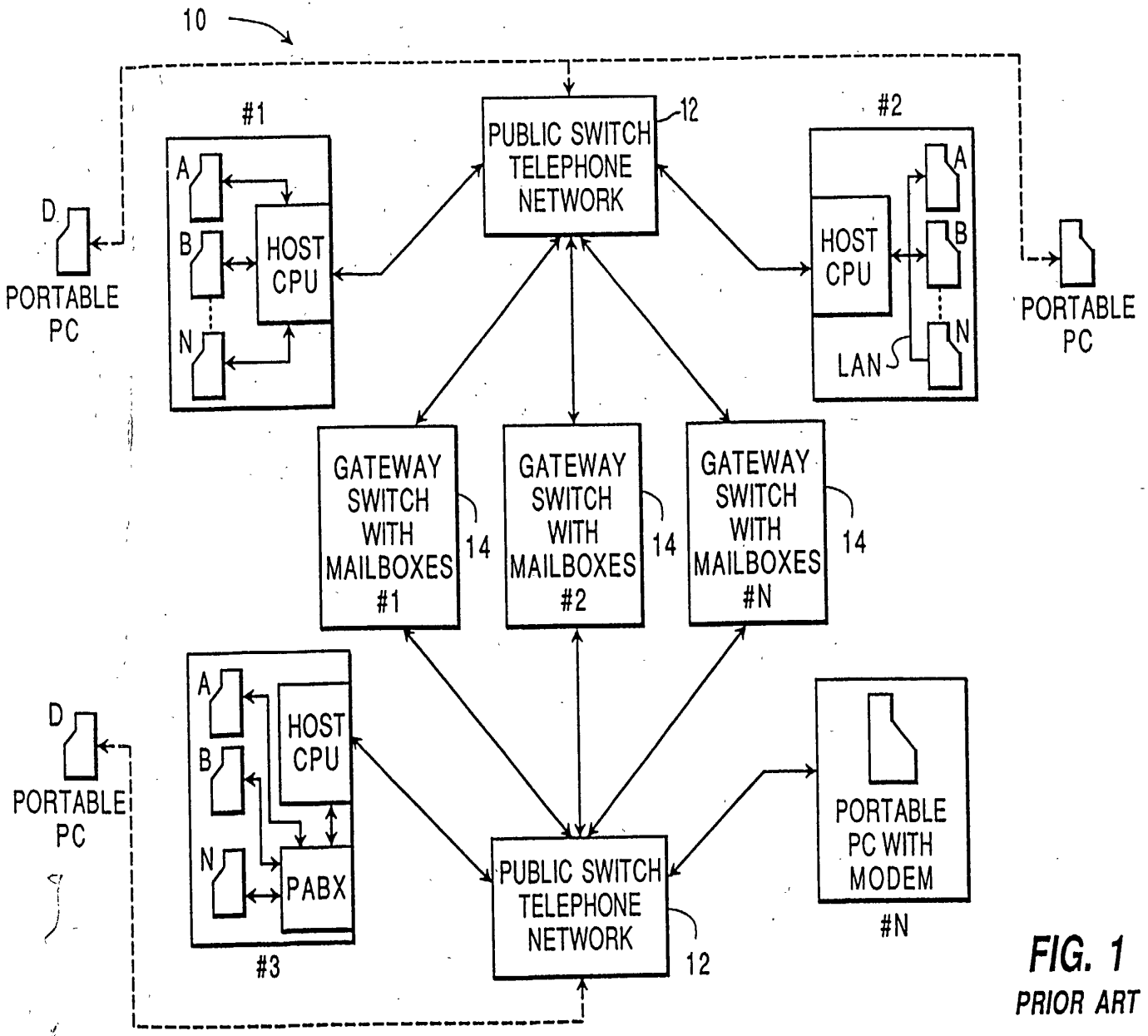

FIG. 1
PRIOR ART

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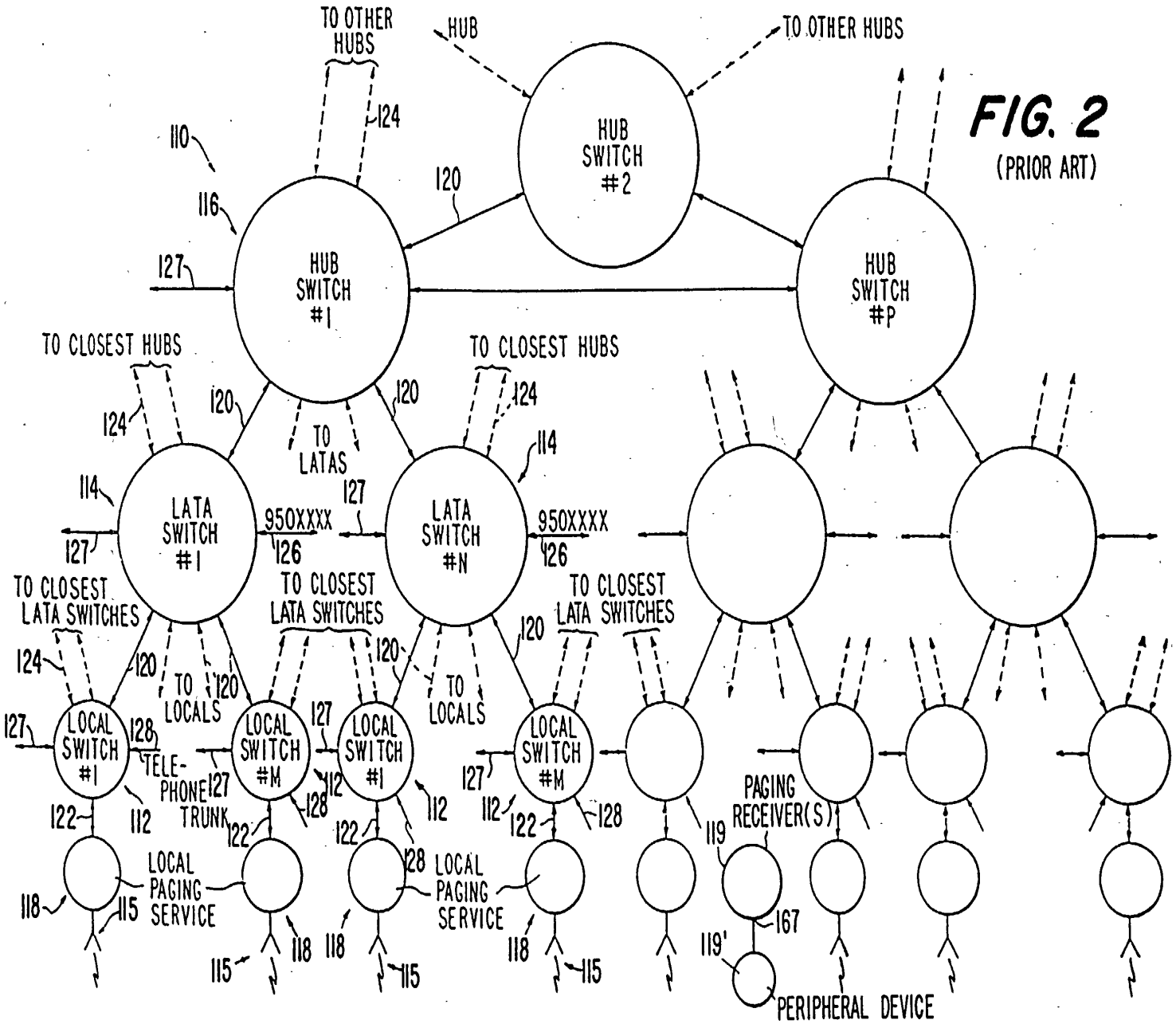

FIG. 2
(PRIOR ART)

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FIG. 3
(PRIOR ART)

LOCAL SWITCH MEMORY MAP

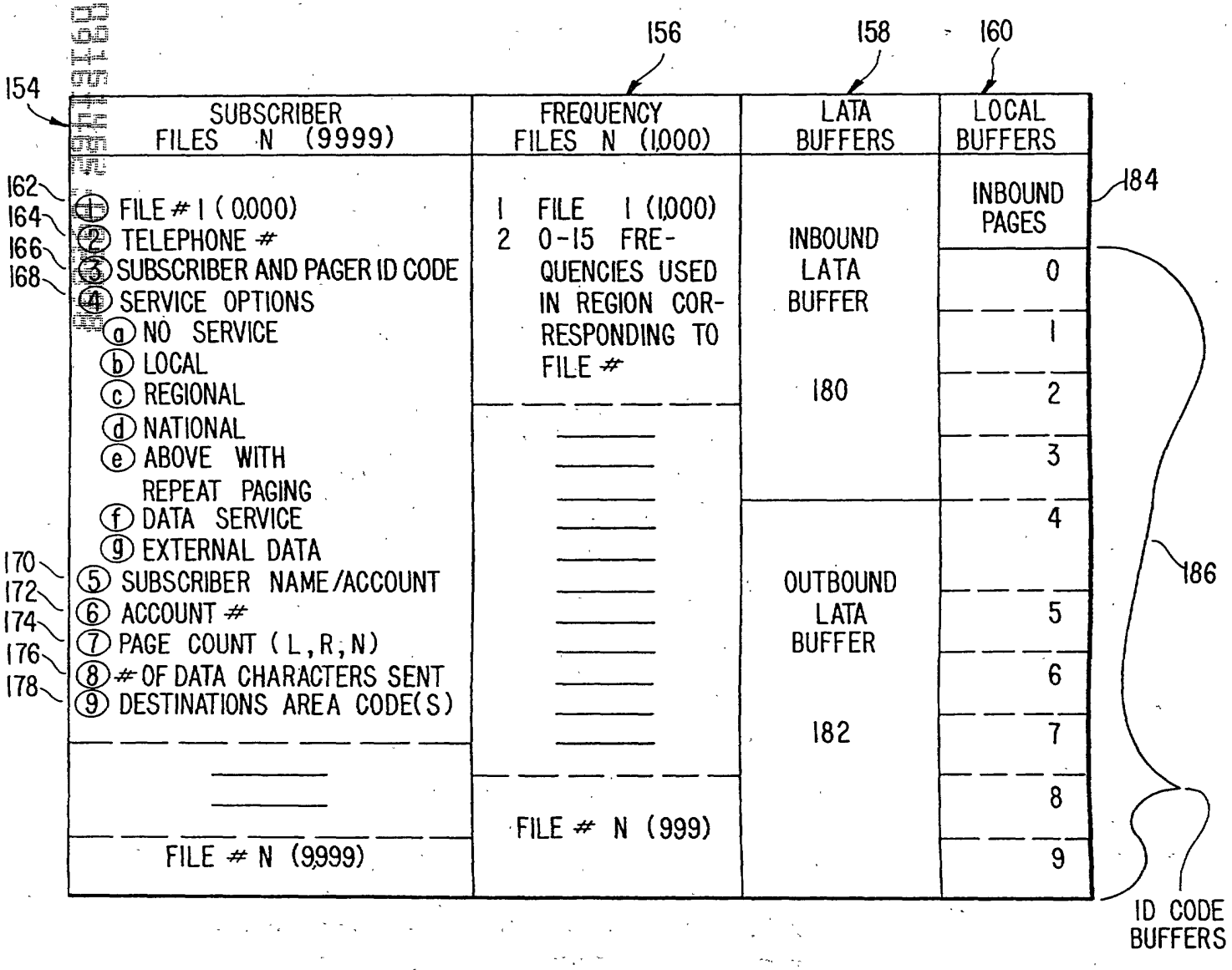

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FIG. 4

(PRIOR ART)

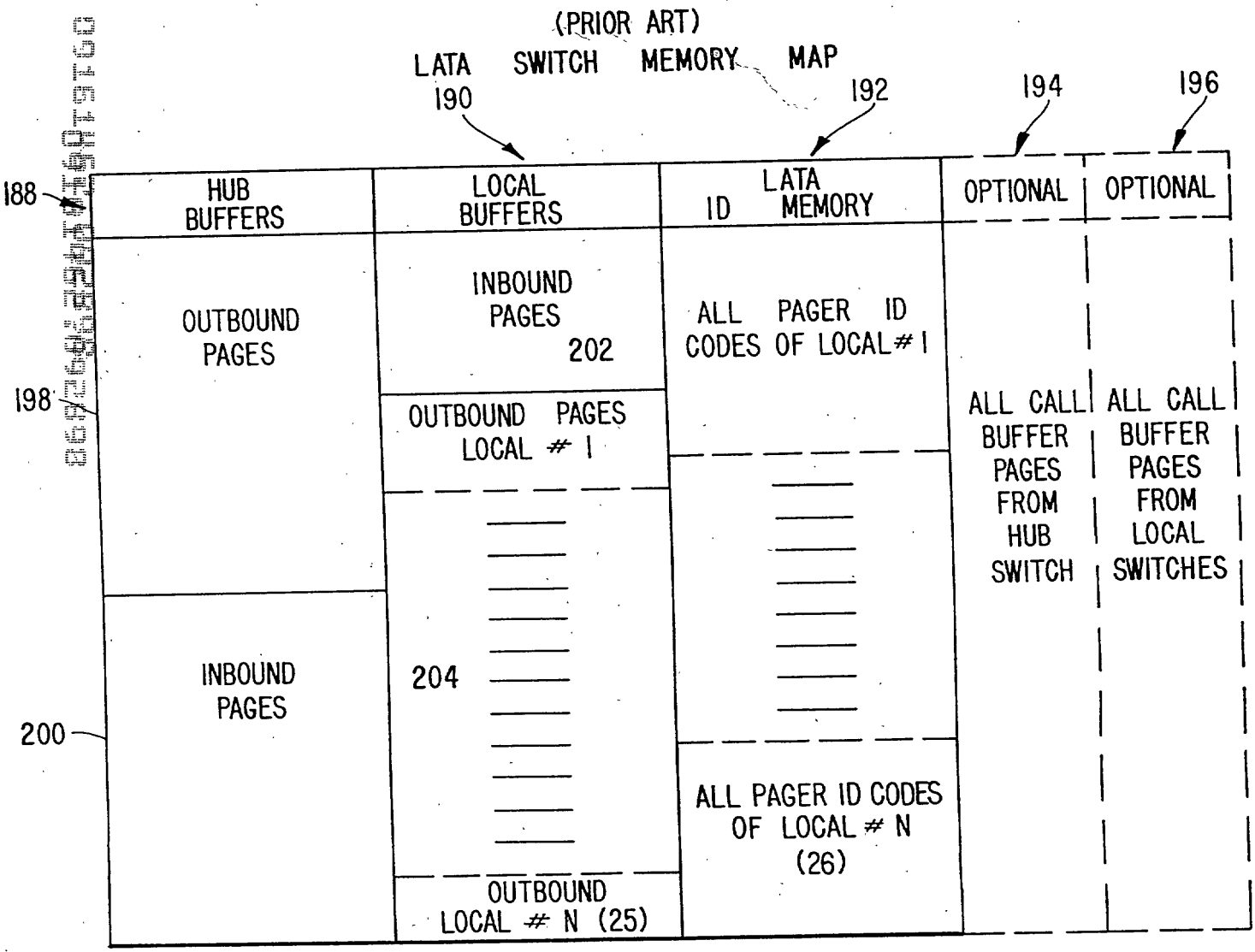

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FIG. 5
(PRIOR ART)

HUB SWITCH MEMORY MAP

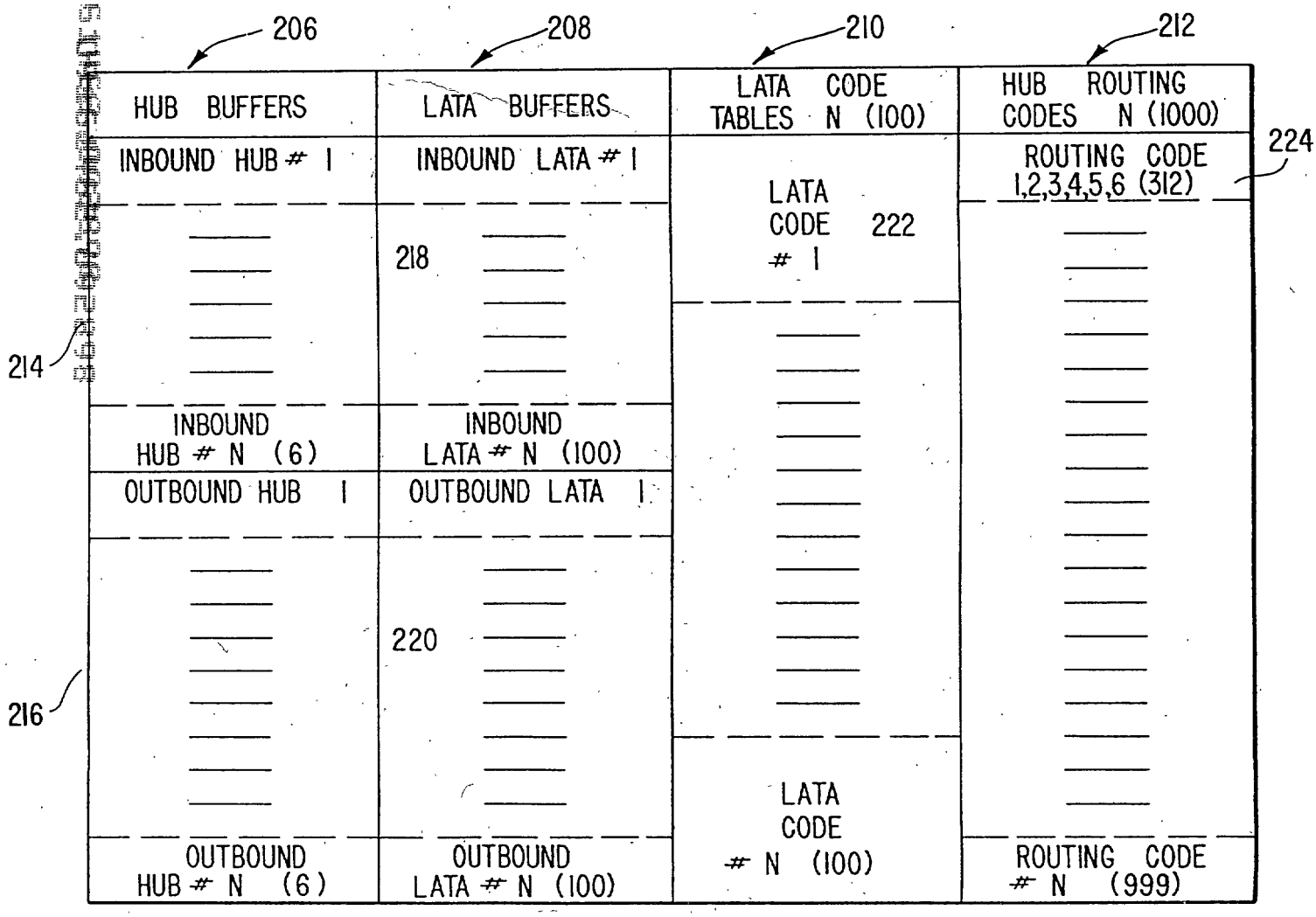

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FIG. 6
PRIOR ART

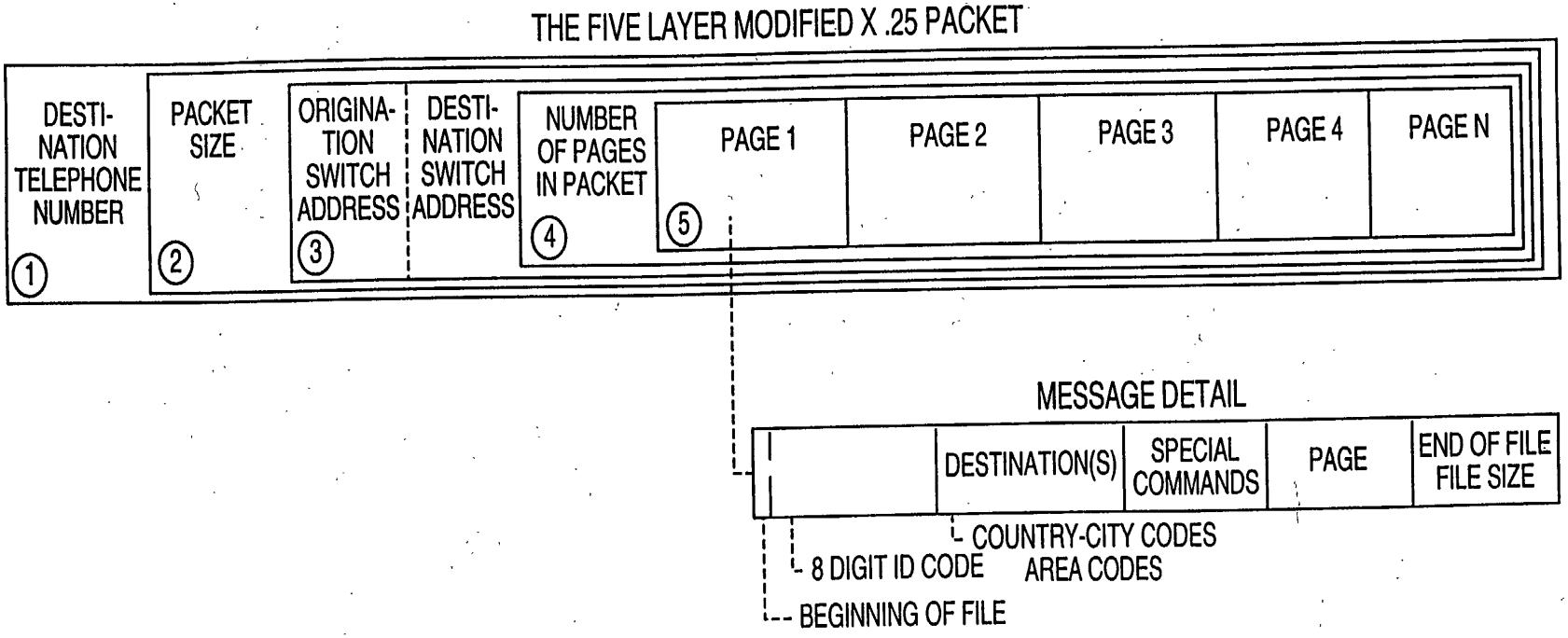

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FIG. 7
PRIOR ART

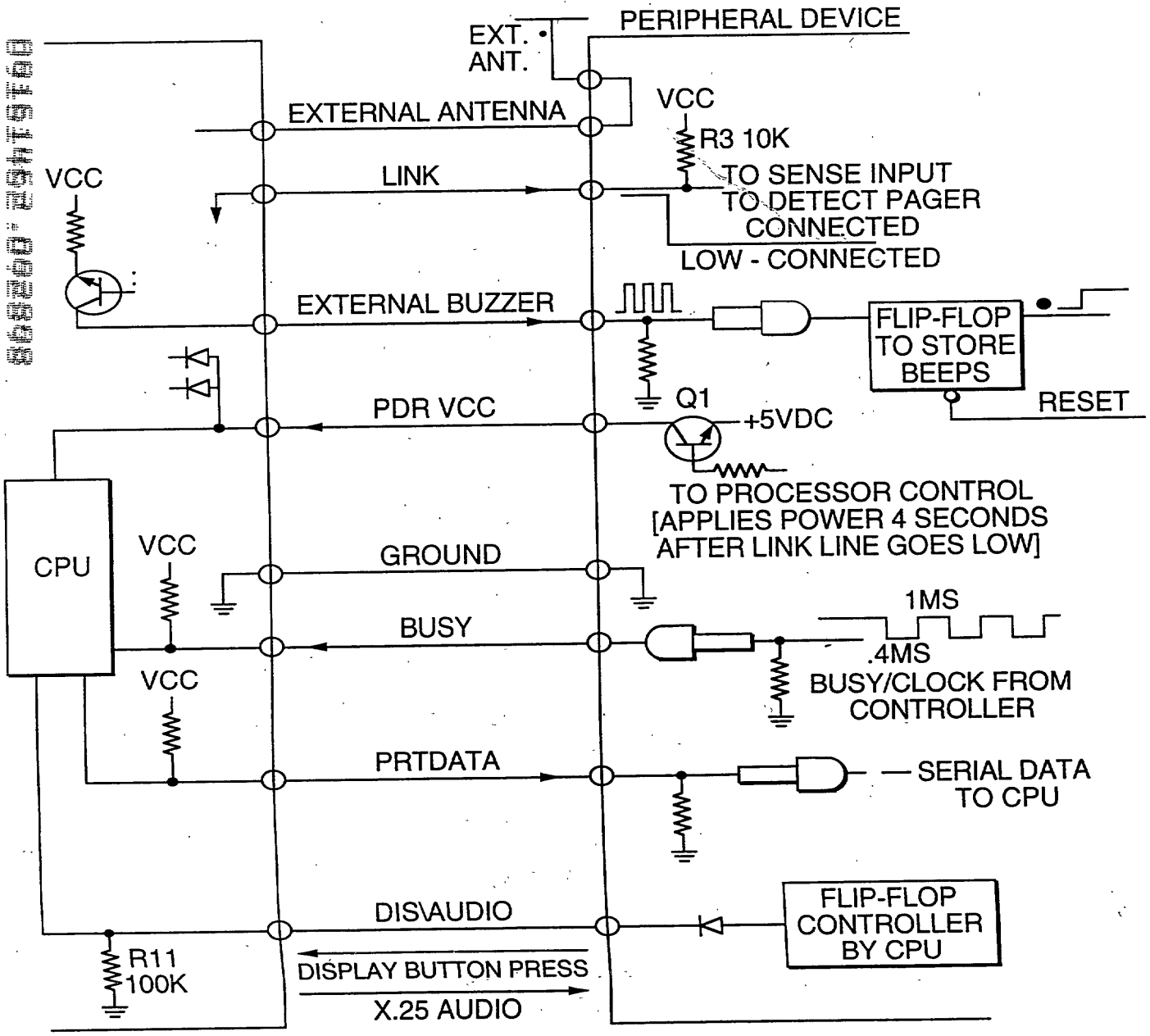

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FIG. 8

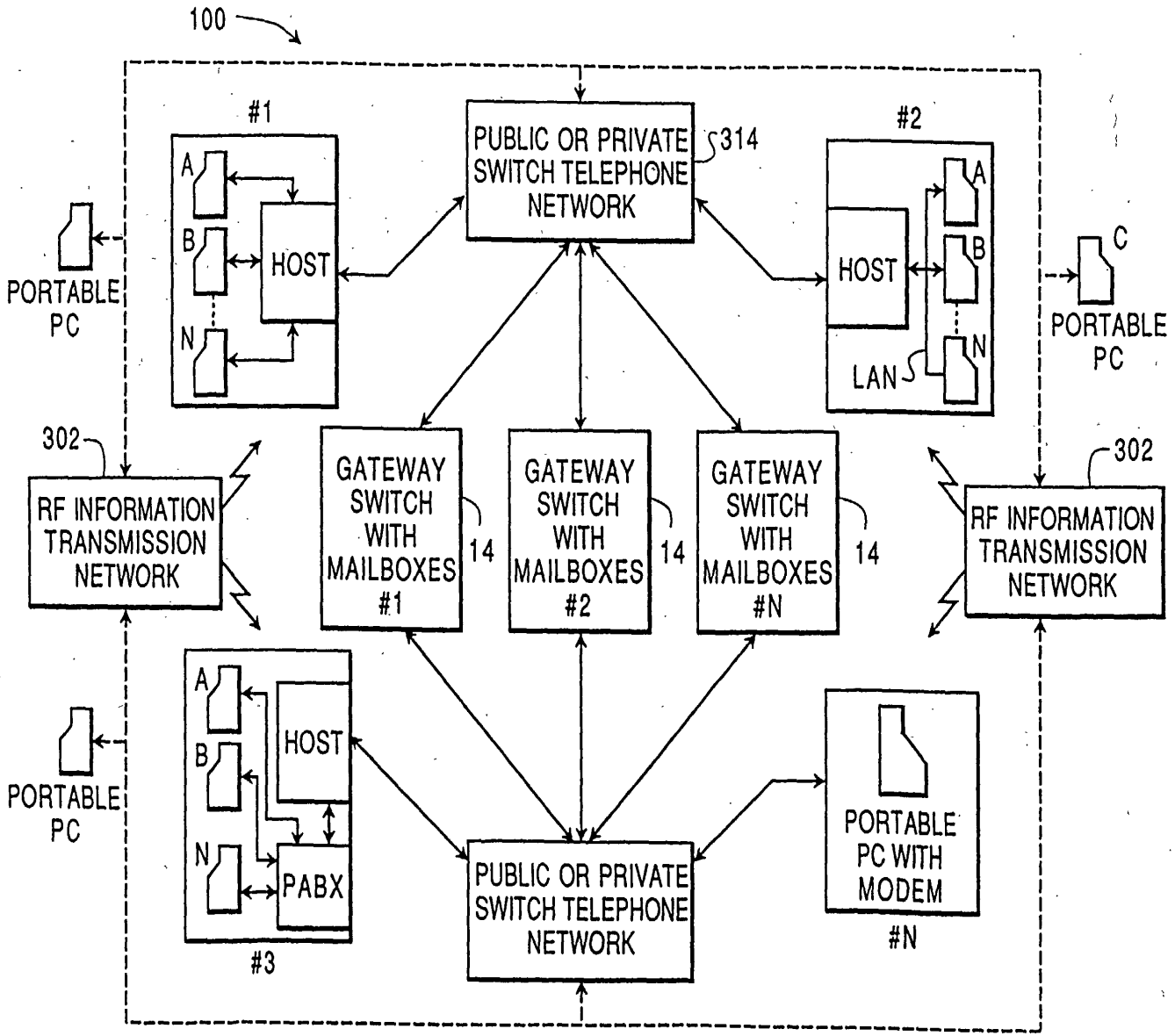

FIG. 9

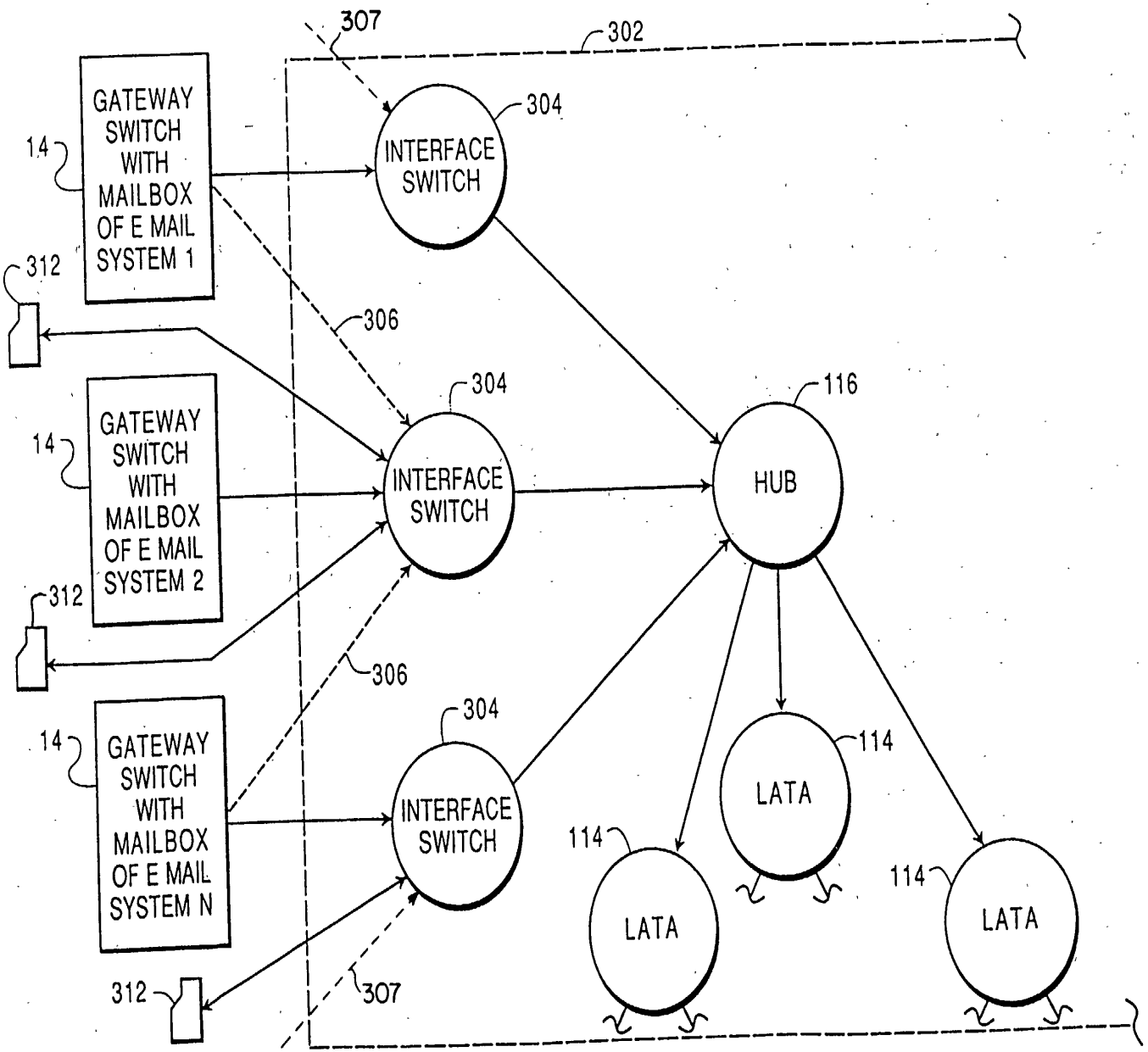

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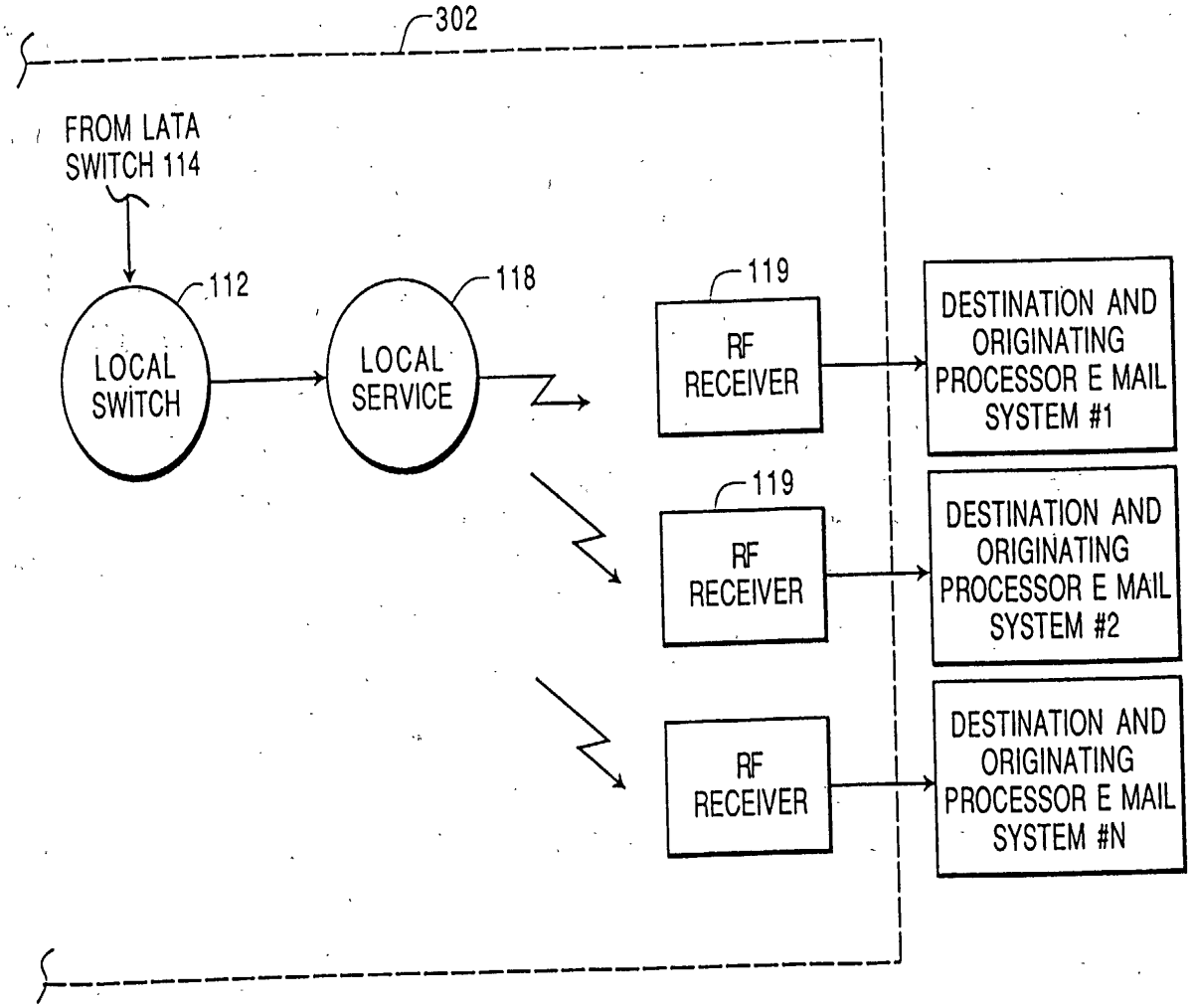
FIG. 10



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868260 '29 T9760

ENTRY METHOD	ORIGINATING PROCESSOR →	GATEWAY SWITCH 14 →	INTERFACE SWITCH 304
1	ADDS INTERFACE (WIRELESS) DESTINATION AND DESTINATION PROCESSOR	NO-ACTION	ADDS ID OF RF RECEIVER 119
2	ADDS INTERFACE (WIRELESS) DESTINATION AND ID OF RECEIVER 119	NO-ACTION	NO ACTION OTHER THAN ID VERIFICATION
3	ADDS DESTINATION PROCESSOR	ADDS WIRELESS DESTINATION	ADDS ID OF RECEIVER 119
4	ADDS DESTINATION PROCESSOR	ADDS WIRELESS DESTINATION AND ID OF RECEIVER 119	NO ACTION OTHER THAN ID VERIFICATION
5	ADDS DESTINATION PROCESSOR, OPERATOR POINTS TO DISPLAYED ICON, ORIGINATING PROCESSOR ADDS WIRELESS DESTINATION.	ADDS ID OF RECEIVER 119	NO ACTION OTHER THAN ID VERIFICATION
6	ADDS DESTINATION PROCESSOR, OPERATOR POINTS TO DISPLAYED ICON, ORIGINATING PROCESSOR ADDS WIRELESS DESTINATION.	NO-ACTION	ADDS ID OF RECEIVER 119
7	ADDS DESTINATION PROCESSOR, OPERATOR POINTS TO DISPLAYED ICON, ORIGINATING PROCESSOR ADDS WIRELESS DESTINATION AND ID OF RECEIVER 119(BY COMPARING DESTINATION PROCESSOR TO ID TABLE).	NO-ACTION	NO ACTION OTHER THAN ID VERIFICATION

FIG. 11

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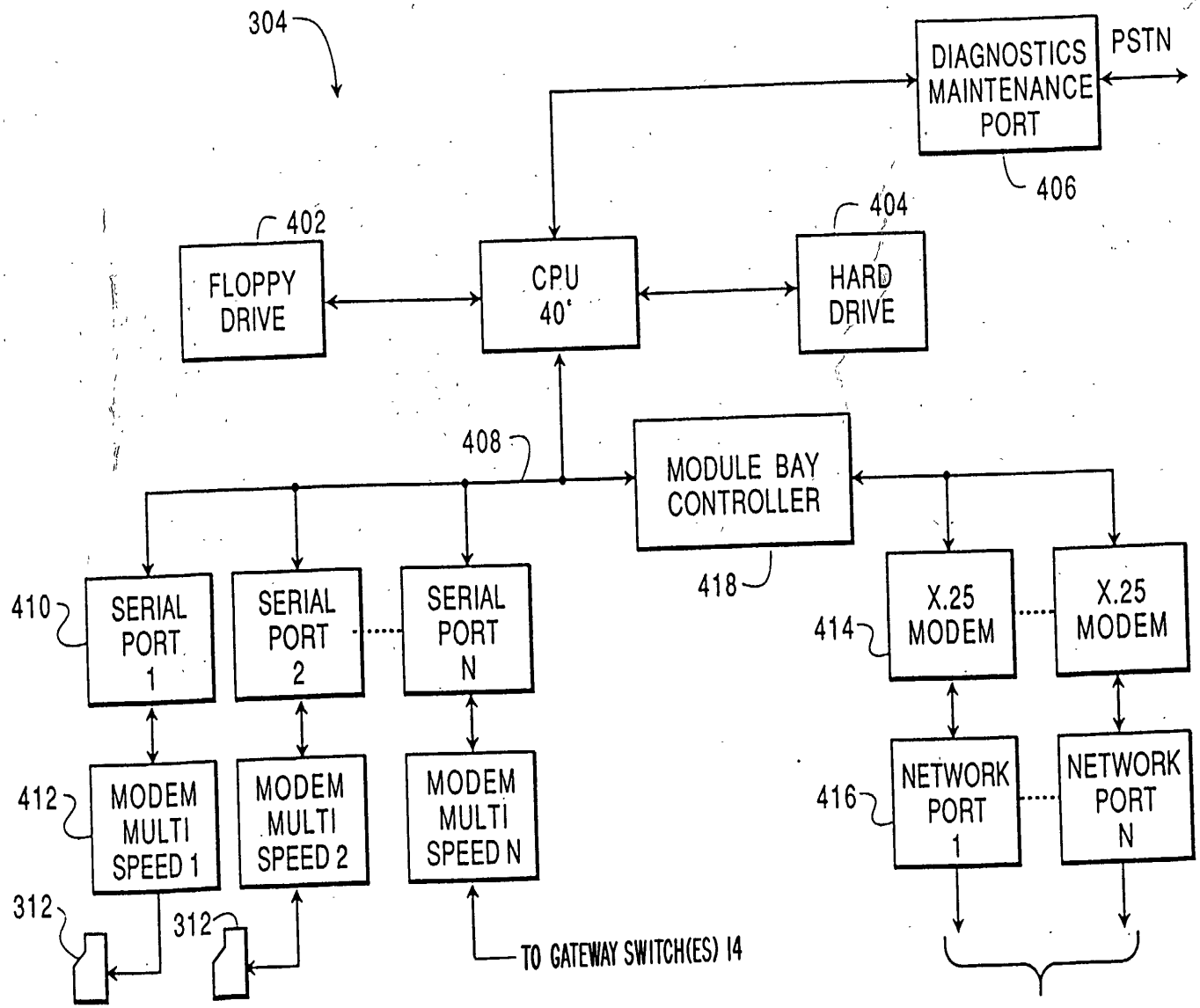
FIG. 12